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**Reply under 37 CFR 1.116
Expedited Procedure
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICEApplicants: Dotson *et al.*

Conf. No.: 1172

Serial No.: 10/602,369

Art Unit: 2825

Filing Date: 06/24/2003

Examiner: To, Tuyen P.

Title: SYSTEM, METHOD AND PROGRAM
PRODUCT FOR POSITIONING I/O
PADS ON A CHIP

Docket No.: END920030008US1
(IBME-0077)

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DO NOT ENTER

TT 03/06/2006
Sir:**AFTER-FINAL AMENDMENT****I. INTRODUCTORY COMMENTS:**

This paper is being filed in response to the Final Office Action dated December 28, 2005.

Please amend the above-referenced patent application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks begin on page 12 of this paper.

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